

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Currently Amended) A variable delay line comprising:

a hybrid coupler having an input terminal for ~~being supplied with~~receiving an input signal, first and second output terminals for outputting first and second output signals, respectively, which are 90° out of phase with each other, and an isolation terminal for outputting a reflected signal based on said first and second output signals as a third output signal; and

first and second reactance parts connected respectively to said first and second output terminals, said first and second reactance parts having substantially the same reactances; reactance,

~~wherein~~said first and second reactance parts havecomprising first and second variable-reactance devices, respectively, and first and second resonant circuits, respectively, each of said resonant circuits being connected between said respective variable-reactance device and a common ground, and said variable-reactance devices having substantially the same reactances; reactance.

2. (Currently Amended) A variable delay line according to claim 1, wherein said first and second variable-reactance partsdevices comprise respective first and second series-connected circuits of first and second capacitors having substantially the same ~~capacitances~~capacitance, and first and second variable-capacitance devices as said first and second variable-reactance devices, and a first resonant circuit and a second resonant circuit.

3. (Currently Amended) A variable delay line according to claim 2, ~~wherein~~further comprising third and fourth series-connected circuits of~~having~~ third

and fourth capacitors and third and fourth variable-capacitance devices as variable-reactance devices, respectively, are said third and fourth series connected circuits being connected in parallel to said first resonant circuit and said second resonant circuit, respectively, such that an input of each of said third and fourth series connected circuit is connected to an input of said respective resonant circuit and outputs of each of said third and fourth series connected circuits and said respective resonant circuits are connected to the common ground.

4. (Currently Amended) A variable delay line according to claim 2; ~~wherein a plurality of ceramic layers are stacked to form~~ further comprising an integral structural body, said integral structural body having a ceramic layer with said hybrid coupler disposed thereon, a ceramic layer with said first resonant circuit and said second resonant circuit disposed thereon, and a ceramic layer with at least said first and second capacitors disposed thereon.

5. (Currently Amended) A variable delay line according to claim 3; ~~wherein a plurality of ceramic layers are stacked to form~~ further comprising an integral structural body, said integral structural body having a ceramic layer with said hybrid coupler disposed thereon, a ceramic layer with said first resonant circuit and said second resonant circuit disposed thereon, and a ceramic layer with at least said first and second capacitors disposed thereon.